

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	41883	serial with (bus line cable) with data	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 13:41
L2	106867	sync\$8 with (buffer\$4 shift\$4 relay\$4 delay\$4 stor\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 13:42
L3	990	1 same 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 13:43
L4	10865	375/211,214,257,354,357, 362-372.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 13:46
L5	51	3 and 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 13:46

Day : Wednesday
Date: 12/21/2005
Time: 13:57:31

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = SONG

First Name = HONGJIANG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09473738	6531896	150	12/28/1999	DUAL MODE TRANSMITTER	SONG, HONGJIANG
09473740	Not Issued	71	12/28/1999	SYNCHRONIZATION DETECTION ARCHITECTURE FOR SERIAL DATA COMMUNICATION	SONG, HONGJIANG
09475486	6466615	150	12/30/1999	DELAY LOCKED LOOP BASED CIRCUIT FOR DATA COMMUNICATION	SONG, HONGJIANG
09475497	6775345	150	12/30/1999	DELAY LOCKED LOOP BASED DATA RECOVERY CIRCUIT FOR DATA COMMUNICATION	SONG, HONGJIANG
09476382	6275555	150	12/30/1999	DIGITAL DELAY LOCKED LOOP FOR ADAPTIVE DE-SKEW CLOCK GENERATION	SONG, HONGJIANG
09476634	6570931	150	12/31/1999	SWITCHED VOLTAGE ADAPTIVE SLEW RATE CONTROL AND SPECTRUM SHAPING TRANSMITTER FOR HIGH SPEED DIGITAL TRANSMISSION	SONG, HONGJIANG
09476978	6639956	150	12/31/1999	DATARESYNCHRONIZATION CIRCUIT	SONG, HONGJIANG
09606688	6847236	150	06/30/2000	BUFFER/VOLTAGE-MIRROR ARRANGEMENTS FOR SENSITIVE NODE VOLTAGE CONNECTIONS	SONG, HONGJIANG
09821649	Not Issued	71	03/29/2001	Locked loop circuit	SONG, HONGJIANG
09867155	6469646	150	05/29/2001	CONVERTING DIGITAL SIGNALS TO ANALOG SIGNALS	SONG, HONGJIANG
09873820	6917660	150	06/04/2001	ADAPTIVE DE-SKEW CLOCK GENERATION	SONG, HONGJIANG
10226945	6633248	150	08/23/2002	CONVERTING DIGITAL SIGNALS TO ANALOG SIGNALS	SONG, HONGJIANG
10461061	Not Issued	20	06/13/2003	Unified bandgap voltage and PTAT current reference circuit	SONG, HONGJIANG
10644203	6838945	150	08/19/2003	DATA RESYNCHRONIZATION CIRCUIT	SONG, HONGJIANG

10668638	Not Issued	30	09/22/2003	Direct down-conversion receiver with transconductance-capacitor filter and method	SONG, HONGJIANG
10812834	Not Issued	30	03/30/2004	Calibration and testing architecture for receivers	SONG, HONGJIANG
10883358	Not Issued	41	06/30/2004	Differential dual port current conveyor circuit	SONG, HONGJIANG
10984584	Not Issued	61	11/09/2004	Buffer/voltage-mirror arrangements for sensitive node voltage connections	SONG, HONGJIANG
11170979	Not Issued	25	06/30/2005	Method and system for link jitter compensation including a fast data recovery circuit	SONG, HONGJIANG
11171860	Not Issued	20	06/30/2005	Voltage controlled oscillator (VCO) tuning	SONG, HONGJIANG

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="SONG"/>	<input type="text" value="HONGJIANG"/>	<input type="button" value="Search"/>

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